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AMENDMENTS TO THE CLAIMS:

1. (Currently amended) A semiconductor device, comprising:
 - a multi-layered insulation film formed on a semiconductor substrate, said multi-layered insulation film comprising:
 - a first insulation layer comprising an organic material having a dielectric constant which is lower than a silicon oxide dielectric constant;
 - a second insulation layer comprising a polysiloxane compound having an Si-H group and formed on and adhering to a top of said first insulation layer; and
 - a third insulation layer comprising an inorganic material and formed on and adhering to a top of said second insulation layer; and
 - a plurality of wires which are formed in grooves embedded in a groove formed in said multi-layered insulation film, said multi-layered insulation film filling a space being disposed between said wires,
wherein said second insulation layer comprises a hydride organosiloxane, and said second insulation layer comprises a layer to improve an adhesion property between said first insulation layer and said third insulation layer.
2. (Previously Presented) The semiconductor device according to claim 1, wherein said first insulation layer comprises at least one of an organosiloxane and an aromatic-containing organic resin.
3. (Canceled)
4. (Previously Presented) The semiconductor device according to claim 1, wherein said third insulation layer comprises at least one material selected from the group consisting of silicon oxide, silicon nitride and silicon oxynitride.
5. (Currently amended) A semiconductor wafer, comprising:

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a multi-layered insulation film formed on a surface of the wafer, said multi-layered insulation film comprising:

a first insulation layer comprising an organic material having a dielectric constant which is lower than a silicon oxide dielectric constant;

a second insulation layer comprising a polysiloxane compound having an Si-H group and formed on and adhering to a top of said first insulation layer; and

a third insulation layer comprising an inorganic material and formed on and adhering to a top of said second insulation; and

a plurality of wires which are formed in grooves embedded in a groove formed in said multi-layered insulation film, said multi-layered insulation film filling a space being disposed between said wires,

wherein said second insulation layer comprises a hydride organopolysiloxane, and said second insulation layer comprises a layer to improve an adhesion property between said first insulation layer and said third insulation layer.

6. (Previously Presented) The semiconductor wafer according to claim 5, wherein said first insulation layer comprises at least one of an organopolysiloxane and an aromatic-containing organic resin.

7. (Canceled)

8. (Previously Presented) The semiconductor wafer according to claim 5, wherein said third insulation layer comprises at least one material selected from the group consisting of silicon oxide, silicon nitride and silicon oxynitride.

9-30 (Canceled)

31. (Previously Presented) The semiconductor device according to claim 1, wherein said

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dielectric constant of said first insulation layer is no greater than 3.5.

32. (Previously Presented) The semiconductor device according to claim 2, wherein said organopolysiloxane comprises at least one of an alkyl silsesquioxane and a hydride alkyl siloxane.

33. (Previously Presented) The semiconductor device according to claim 2, wherein said aromatic-containing organic resin comprises at least one of a polyaryl ether and a divinyl siloxane-bis-benzocyclobutene.

34. (Previously Presented) The semiconductor device according to claim 1,
wherein said first insulation layer comprises a thickness greater than a thickness of said second insulation layer, and
wherein said first insulation layer comprises a thickness greater than a thickness of said third insulation layer.

35. (Previously Presented) The semiconductor device according to claim 1, wherein said second insulation layer comprises a first layer and a second layer placed in said first layer.

36. (Previously Presented) The semiconductor device according to claim 1, wherein said first insulation layer comprises methyl silsesquioxane.

37. (Previously Presented) The semiconductor device according to claim 1, wherein a bottom of said groove is formed on a same surface as said first insulation layer.

38. (Previously Presented) The semiconductor device according to claim 1, wherein said plurality of wires comprise copper wires.

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39. (Previously Presented) The semiconductor device according to claim 42, wherein said second insulation layer comprises a methylated hydrogen silsesquioxane (MHSQ) film.

40. (Previously Presented) The semiconductor device according to claim 42, wherein said MHSQ film comprises a thickness of about 50 nm.

41. (Currently amended) A semiconductor device having a damascene wiring structure, said semiconductor device comprising:

a multi-layered insulation film formed on a semiconductor substrate, said multi-layered insulation film having a plurality of recesses and comprising:

a first insulation layer comprising an organic material having a dielectric constant which is lower than a silicon oxide dielectric constant;

a second insulation layer comprising a polysiloxane compound having an Si-H group and formed on and adhering to a top of said first insulation layer; and

a third insulation layer comprising an inorganic material and formed on and adhering to a top of said second insulation layer; and

an electroconductive film formed in a each recess in said plurality of recesses, said multi-layered insulation film filling a space being disposed between recesses each recess in said plurality of recesses,

wherein said second insulation layer comprises a hydride organosiloxane, and said second insulation layer comprises a layer to improve an adhesion property between said first insulation layer and said third insulation layer.

42. (Currently amended) A semiconductor device comprising a multi-layered insulation film and a plurality of wires formed on a semiconductor substrate, said multi-layered insulation film comprising:

a first insulation layer comprising an organic material having a dielectric constant which is lower than a silicon dioxide dielectric constant;

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a second insulation, adhesive layer comprising a polysiloxane compound having an Si-H group and formed on and being in contact with a top of said first insulation layer; and

a third insulation layer comprising an inorganic material and formed on and being in contact with a top of said second insulation, adhesive layer,

wherein said multi-layered insulation film fills a space is disposed between said wires in said plurality of wires,

wherein said plurality of wires are formed in grooves which are embedded in a groove formed in said multi-layered insulation film, and

wherein said second insulation layer comprises a hydride organosiloxane, and said second insulation layer comprises a layer to improve an adhesion property between said first insulation layer and said third insulation layer.

43. (Previously Presented) The semiconductor device according to claim 1, wherein said first insulation layer, said second insulation layer and said third insulation layer of said multi-layered insulation film comprise substantially uniform widths.

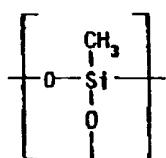
44. (Previously Presented) The semiconductor device according to claim 1, wherein a surface of said multi-layered film is substantially coplanar with a surface of said plurality of wires.

45. (Previously Presented) The semiconductor device according to claim 1, wherein said second insulation layer is formed by one of a plasma CVD and a spin coating process where said semiconductor substrate is continuously maintained in a plasma atmosphere.

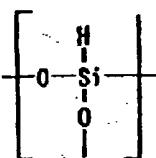
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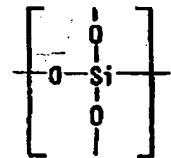
46. (Previously Presented) The semiconductor device according to claim 1, wherein said hydride organosiloxane comprises methylated hydrogen silsesquioxane including repeating units shown by formulae I, II and III



(I)



(II)



(III)

, and

wherein a molar ratio of [II] to a total of I, II and III is at least 0.2.

47. (New) The semiconductor device according to claim 1, wherein a greatest thickness of said second insulation layer is less than a thickness of said first insulation layer.

48. (New) The semiconductor device according to claim 1, wherein said second insulation layer comprises a thin layer for adhering said third insulation layer to said first insulation layer.

49. (New) A semiconductor device, comprising:

a multi-layered insulation film having a plurality of grooves formed therein, said multi-layered insulation film comprising:

a first insulation layer formed on a substrate and comprising an organic material having a dielectric constant which is lower than a silicon oxide dielectric constant;

a second insulation layer comprising a polysiloxane compound having an Si-H group and formed on said first insulation layer; and

a third insulation layer comprising an inorganic material and formed on said second insulation layer; and

a plurality of wires which are formed in grooves formed in said multi-layered insulation film, said multi-layered insulation film filling a space between said wires.

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50. (New) The semiconductor device according to claim 49, further comprising:
a plurality of gate electrodes formed on said substrate, said first insulation layer being
formed on and between said gate electrodes,

wherein said plurality of wires comprises a contact which contacts a diffusion region
formed in said substrate between said plurality of gate electrodes, and

wherein a space formed between adjacent gate electrodes in said plurality of gate
electrodes is filled with said first insulation layer.

51. (New) The semiconductor device according to claim 50, wherein a space formed between
adjacent gate electrodes in said plurality of gate electrodes is filled with said first insulation layer.

52. (New) The semiconductor device according to claim 1, wherein said first, second and
third insulation films of said multi-layered insulation film fill a space between said wires.